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Docket: 0756-1551

671155  
6/19/02  
A. J. S.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of )  
SHUNPEI YAMAZAKI et al )  
Serial No. 08/691,434 ) Group Art Unit: 2822  
Filed: 08/02/1996 ) Examiner: Wilczewski  
For: METHOD OF FABRICATING )  
SEMICONDUCTOR DEVICES AND )  
APPARATUS FOR PROCESSING A )  
SEMICONDUCTOR )

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INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith attached Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c).

Respectfully submitted,

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180.00 OP

  
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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 5/20/02  
